High Reliability Photologic® Optically Coupled Isolator

OPI125 (TX, TXV)



Features:

- High current transfer ratio
- 15 kV electrical isolation
- Direct TTL/LSTTL interface
- High noise immunity
- Data rates to 250 KBaud
- TX and TXV devices processed to MIL-PRF-19500



Description:

Each **OPI125TX** and **OPI125TXV** is an optically coupled isolator that consists of a gallium aluminum arsenide infrared light emitting diode (OP235 TX or OP235TXV) and a monolithic integrated circuit which incorporates a photodiode, a linear amplifier and a Schmitt trigger on a single die (OPL800TX or OPL800TXV), sealed in a high dielectric plastic housing. The device features TTL/LSTTL compatible logic level output which can drive up to 8 TTL loads directly without additional circuitry. Also featured are medium speed data rates to 250 KBaud with typical rise and fall times of 70 nanoseconds. These devices are designed for applications that require high voltage isolation between input and output.

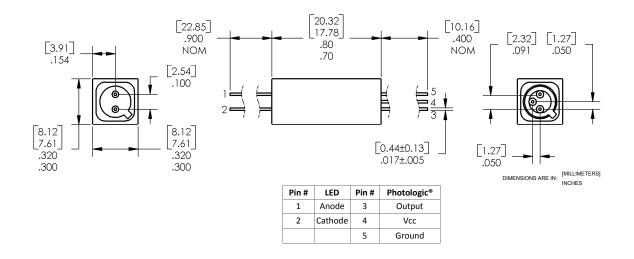
TX and TXV device components are processed to OPTEK's military screening program patterned after MIL-PRF-19500.

Please refer to Application Bulletins 208 and 210 for additional design information and reliability (degradation) data.

Applications:

- Requiring high voltage isolation between input and output
- Electrical isolation in dirty environments
- Industrial equipment
- Medical equipment

Part Number	LED Peak Wavelength	Sensor Photologic®	Isolation Voltage (,000)	t _{PLH} / t _{PHL} Typ (ns)	I _F (mA) Typ / Max	V _{CE} (Volts) Max	Lead Length / Spacing
OPI125TX	890 nm	Totom Dolo	15	5/5	7.5 / 25	35.0	0.40" / 0.75"
OPI125TXV	890 11111	Totem Pole	15	5/5	7.5 / 25	35.0	0.40 / 0.75



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Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Operating Temperature Range	-65° C to +150° C
Storage Temperature Range	-55° C to +125° C
Input-to-Output Isolation Voltage ⁽¹⁾	± 15 k VDC
Lead Soldering Temperature [1/16 inch (1.6mm) fro case for 5 seconds with soldering iron] (1)	260° C
Power Dissipation ⁽²⁾	250 mW

Input Diode

Forward DC Current	100 mA
Reverse Voltage	2 V
Power Dissipation ⁽²⁾	200 mW

Output Phototransistor

Continuous Collector Current	50 mA
Collector-Base Voltage	30 V
Collector-Emitter Voltage	30 V
Emitter-Base Voltage	5 V
Power Dissipation ⁽²⁾	250 mW

Electrical Characteristics (T_A = 25°C unless otherwise noted)

Input Diode

SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
	Forward Voltage ⁽⁴⁾	0.90	1.25	1.50	V	I _F = 10 mA
V _F		1.00	1.45	1.70		I _F = 10 mA, T _A = -55° C
		0.70	1.10	1.30		I _F = 10 mA, T _A = 100° C
I _R	Reverse Current	-	0.10	10	μΑ	V _R = 2 V
Photologic®						
V _{cc}	Operating Supply Voltage	4.8		5.2	V	
I _{cc}	Supply Current		7.0	15.0	mA	V _{CC} = 5.2V, I _F = 0 or 7.5 mA

Notes:

(1) Measured with input leads shorted together and output leads shorted together in air with a maximum relative humidity of 50%. If suitably encapsulated or oil-immersed, the isolation voltage is increased to at least 25 kV.

Derate linearly 2.0 mW/° C above 25° C.

⁽³⁾ Derate linearly 2.5 mw/° C above 25° C

⁽⁴⁾ Measurement is taken during the last 500 µs of a single 1.0 ms test pulse. Heating due to increased pulse rate or pulse width can cause change in measurement results.

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Electrical Characteristics (T _A = 25°C unless otherwise noted)							
SYMBOL	MBOL PARAMETER		TYP	MAX	UNITS	TEST CONDITIONS	
Coupled							
I _F (+)	LED Positive-Going Threshold Current ⁽¹⁾	-	-	7.5	mA	V _{CC} = 5.0 V	
		-	-	7.5	mA	V _{CC} = 5.0 V, T _A = -55°C	
		-	-	7.5	mA	V _{CC} = 5.0 V, T _A = 100°C	
$I_{F(+)}/I_{F(-)}$	Hysteresis Ratio	-	2.0	-	-	-	
V _{OL}	Low Level Output Voltage	-	-	4.0	V	V _{CC} = 4.8 V, I _{OL} = 13.0 mA, I _F = 0	
V _{OH}	High Level Output Voltage	2.4	-	-	V	$V_{CC} = 4.8 \text{ V}, I_{OH} = -800 \mu\text{A}, I_{F} = 7.5 \text{mA}$	
I _{OS}	Short Circuit Output Current	-30.0	-	-120	mA	V _{CC} = 5.2 V, I _F = 7.5 mA, Output = GND	
V _{IOS}	Isolation Voltage (Input-to-Output)	15.0			kV	See Note 1	
t _r , t _f	Output Rise Time and Fall Time	-	70.0	100	ns	V _{CC} = 5.0 V, T _A = 25°C	
t _{PLH} , t _{PHL}	Propagation Delay, Low to High & High to Low	-	5.0	10.0	μς	I _F = 0 or 10.0 mA I _F = 10.0 kHz, DC = 50% R _L = 8 TTL Loads	

Notes:

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⁽¹⁾ Measured with input leads shorted together and output leads shorted together in air witMeasurement is taken during the last 500 µs of a single 1.0 ms test pulse. Heating due to increased pulse rate or pulse width can cause change in measurement results.