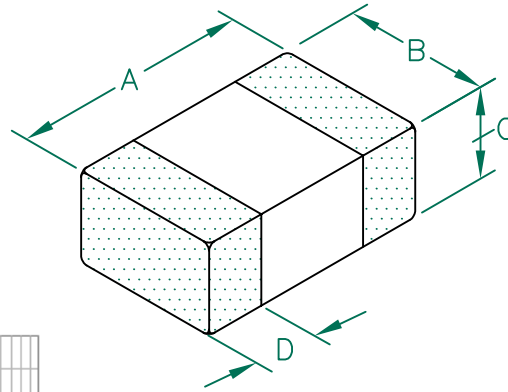


# CPI0805IR82R-10

**UNCONTROLLED DOCUMENT**

PHYSICAL DIMENSIONS:

A	2.00 [.079]	$\pm$	0.20[.008]
B	1.25 [.049]	$\pm$	0.20[.008]
C	0.90 [.035]	$\pm$	0.10[.004]
D	0.50 [.020]	$\pm$	0.20[.008]



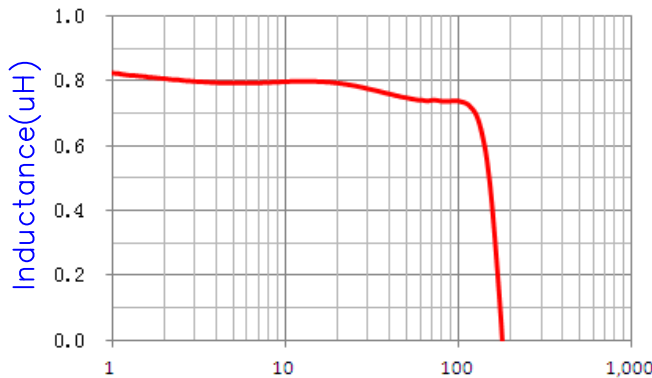
ELECTRICAL CHARACTERISTICS:

	L ( $\mu$ H) @ 1MHz $\pm$ 20%	DCR ( $\Omega$ ) $\pm$ 25%	I (Max)
Nom	0.82	0.14	
Min	0.66	0.105	
Max	0.98	0.175	900mA

NOTES: UNLESS OTHERWISE SPECIFIED

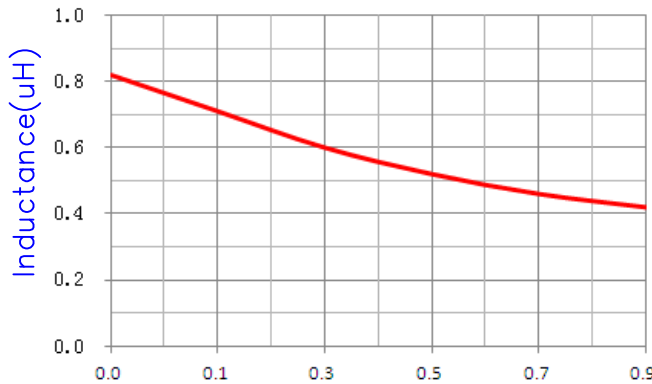
1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER TAPE.
2. TERMINATION FINISH IS 100% MATTE Sn OVER Ni.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. I (MAX.) IS BASED ON THE MAXIMUM SUSTAINED CURRENT APPLIED WHILE MAINTAINING A MAXIMUM TEMPERATURE RISE OF 40°C OVER AMBIENT.
5. OPERATION TEMPERATURE TEMP: -55°C~+125°C (INCLUDING SELF-HEATING)

Ls vs Frequency



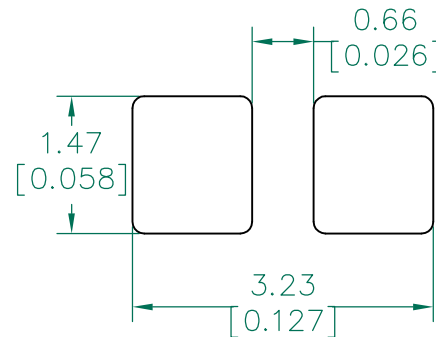
Frequency (MHz)

Ls vs DC BIAS Current



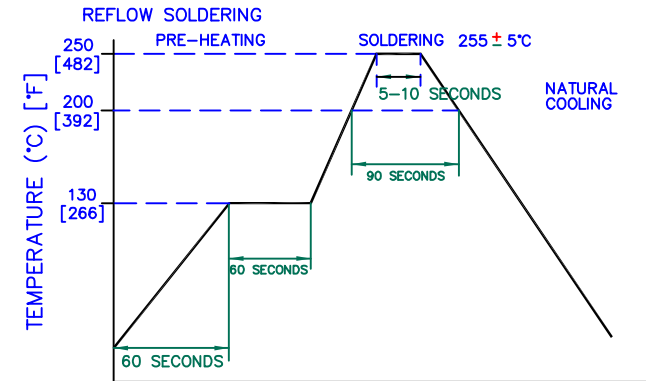
DC BIAS Current (A)

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.763 [0.030] to this dimension)

RECOMMENDED SOLDERING CONDITIONS



DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.			
				PROJECT/PART NUMBER:	REV	PART TYPE:	DRAWN BY:
C	CHANGE PLASTIC TAPE TO PAPER TAPE	04/17/14	QU	CPI0805IR82R-10	C	CO-FIRE	QU
B	UPDATE LAIRD LOGO AND NOTES 5	08/05/13	QU	DATE: 03/01/11	SCALE: NTS	SHEET:	
A	ORIGINAL DRAFT	03/01/11	QU	CAD #	TOOL #	1 of 1	
REV	DESCRIPTION	DATE	INT	CPI0805IR82R-10-A			