

XCZU3EG-L2SFVA625E

Part Number: **XCZU3EG-L2SFVA625E**
 Product Description: IC FPGA 180 I/O 625FCBGA
 RoHs Status: Lead free / RoHS Compliant
 Datasheets: [1.XCZU3EG-L2SFVA625E.pdf](#)
[2.XCZU3EG-L2SFVA625E.pdf](#)
[3.XCZU3EG-L2SFVA625E.pdf](#)
[4.XCZU3EG-L2SFVA625E.pdf](#)

Manufacturer/Brand: Xilinx
 Ship From: Hong Kong
 Shipment Way: DHL/Fedex/TNT/UPS/EMS



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Image may be representation.
 See specifications for product details.

Product details

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Supplier Device Package	625-FCBGA (21x21)	Speed	533MHz, 600MHz, 1.3GHz
Series	Zynq@ UltraScale+™ MPSoC EG	RAM Size	256KB
Primary Attributes	Zynq@UltraScale+™ FPGA, 154K+ Logic Cells	Peripherals	DMA, WDT
Packaging	Tray	Package / Case	625-BFBGA, FCBGA
Operating Temperature	0°C ~ 100°C (TJ)	Number of I/O	180
Moisture Sensitivity Level (MSL)	4 (72 Hours)	Manufacturer Standard Lead Time	10 Weeks
Lead Free Status / RoHS Status	Lead free / RoHS Compliant	Flash Size	-
Detailed Description	Quad ARM® Cortex®-A53 MPCore™ with CoreSight™, Dual ARM®Cortex™-R5 with CoreSight™, ARM Mali™-400 MP2 System On Chip (SOC) IC Zynq® UltraScale+™ MPSoC EG Zynq@UltraScale+™ FPGA, 154K+ Logic Cells 256KB 533MHz, 600MHz, 1.3GHz 625-FCBGA (21x21)	Core Processor	Quad ARM® Cortex®-A53 MPCore™ with CoreSight™, Dual ARM®Cortex™-R5 with CoreSight™, ARM Mali™-400 MP2
Connectivity	CANbus, EBI/EMI, Ethernet, I²C, MMC/SD/SDIO, SPI, UART/USART, USB OTG	Architecture	MCU, FPGA

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